



Course Instructor

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FLEXNet Professional Development

Analog and Mixed Signal IC Test/Validation Training

There is a need for US engineers trained in analog and mixed signal IC test and validation techniques for analog and mixed signal ICs. This need is for the knowledge to characterize of the behaviors of specific analog and mixed signal IC parts, beyond the use of the ATE systems. New low cost NI STS testers enable direct engineering validation of IC part performance on the engineer's work bench.

Target Audience

This course is provided for engineers and scientists who need to know how to test and validate the performance of analog, power and mixed-signal (mostly analog) ICs. Example attendees would include new hires and experienced engineers who have analog and power IC design and test responsibility and require IC test data to perform their jobs. Additional attendees would include analog system designers who need to know how to compare, through measurement, the analog integrated circuits of different vendors, to verify IC data sheet performance and further test ICs beyond the reported data sheet capability. Moreover, analog IC and sensor test engineers with direct test experience using the NI STS tester all will benefit from the training.

Course Topics

1. Basic Analog Testing (2 days)
 - Includes testing resistors and capacitors using the NI STS Tester, low dropout regulator (LDO) test and validation, and op amp test and validation with a false summing junction
2. Op Amp Testing (2.5 days)
 - Includes op amp test and validation with a nulling amplifier loop and instrumentation amplifier test and validation
3. Sensor and Mixed Signal Testing (2 days)
 - Includes analog & digital temperature sensor IC test and validation

Begins: TBD • Ends: TBD

Per Day: \$400/Attendee • Enrollment Limit: 12

Private registration link will be sent to each attendee